A picture containing text, electronics, computer, display

Description automatically generated

Trigger Continue to go back to the 1st Pause state

The value of A is set by SW

10’b 00 1010 1110 (16’h00AE)

The value of B is set by SW

10’b 00 0110 0111 (16’h0067)

The value of A is set by SW

10’b 00 1010 1010 (16’h00AA)

The value of B is set by SW

10’b 00 0101 0101 (16’h0055)

Switches are first set to 10’b 00 0000 0000

Trigger Continue to go into the 2nd Pause state

Trigger Continue to go into the 3rd Pause state

The program goes into 3rd Pause state

A XOR B result is displayed on HEX Units

Wait for Continue to be triggered to start another XOR Test

The program goes into 2nd Pause state

Wait for user to set the value of B from SW

Wait for Continue to be triggered to start Execution

The program goes into 1st Pause state

Wait for user to set the value of A from SW

Wait for Continue to be triggered

Switches are set to 10’b 00 0001 0100

(16’h0014) to jump to the start address x0014 of the XOR Test program

Reset the entire SLC-3 processor

All registers are cleared

FSM is set to Halted state